

Notice of Allowability	Application No.	Applicant(s)	
	10/617,662	WATAMURA, SHIGEKI	
	Examiner Tarifur R. Chowdhury	Art Unit 2871	

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address--*

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to election filed on 06/16/05.
2. The allowed claim(s) is/are 1 and 21-23.
3. The drawings filed on _____ are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 04/26/05
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

EXAMINER'S AMENDMENT/COMMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

This application is in condition for allowance except for the presence of claims 24-39 to an invention non-elected without traverse. Accordingly, claims 24-39 been cancelled.

The title has been changed as follows:

--Display device having island-shaped conductor for repairing line disconnection-

Comment:

Figs. 7-11 should be labeled as --prior art--.

Election/Restrictions

1. Claim 21 depends from the allowable claim 1 and thus claim 21 is rejoined.

Allowable Subject Matter

2. Claims 1 and 21-23 are allowed.

3. The following is an examiner's statement of reasons for allowance:

4. The prior arts of record do not anticipate or render obvious to one skilled in the art a display device having a plurality of pixels, comprising various elements as claimed, more specifically a plurality of island-shaped conductors for repairing gate line disconnection being provided above the gate line with the first insulating layer interposed therebetween, the plurality of island-shaped conductors provided between and adjacent the source line, a second insulating layer on the plurality of island-shaped conductors and a pixel electrode partially overlapping the plurality of island-shaped conductors provided above the switching element with the second insulating layer interposed between and connected with the switching element via the through hole.

5. The references cited in the IDS filed on 04/26/05 do not read on the claimed invention. For example, US 202/0047977 discloses an in-plane switching type LCD device wherein the data line is repaired by forming dummy patterns. US 2001/0013910 an LCD device includes at least one repair line to repair line defects wherein the first repair line extends from the data line and crosses the gate line and a second repair line overlaps the free end of the first repair line and the data line. US 2002/0050967 is related to an invention to repair drain signal lines wherein a gate signal line and a repair

conductive layer is formed on a substrate and a gate insulating layer is formed to cover the gate lines and the repair conductive layer. USPAT 5,969,779 discloses a liquid crystal display having repair lines. EP 0373586 discloses a liquid crystal display device wherein the broken source lines are repaired by connecting the repair conductive layer at both sides of the broken portion by laser welding. However, none of the above references alone or in combination teach or suggest the claimed display device wherein a plurality of island-shaped conductors for repairing gate line disconnection being provided above the gate line with the first insulating layer interposed therebetween, the plurality of island-shaped conductors provided between and adjacent the source line, a second insulating layer on the plurality of island-shaped conductors and a pixel electrode partially overlapping the plurality of island-shaped conductors provided above the switching element with the second insulating layer interposed between and connected with the switching element via the through hole.

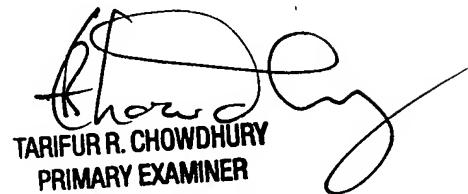
6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R. Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRC
June 22, 2005



TARIFUR R. CHOWDHURY
PRIMARY EXAMINER